

System And Method For Real Time Monitoring And  
Control Of Networked Computers

Inventor: Robert F. Terry

Serial No.: 09/827,891

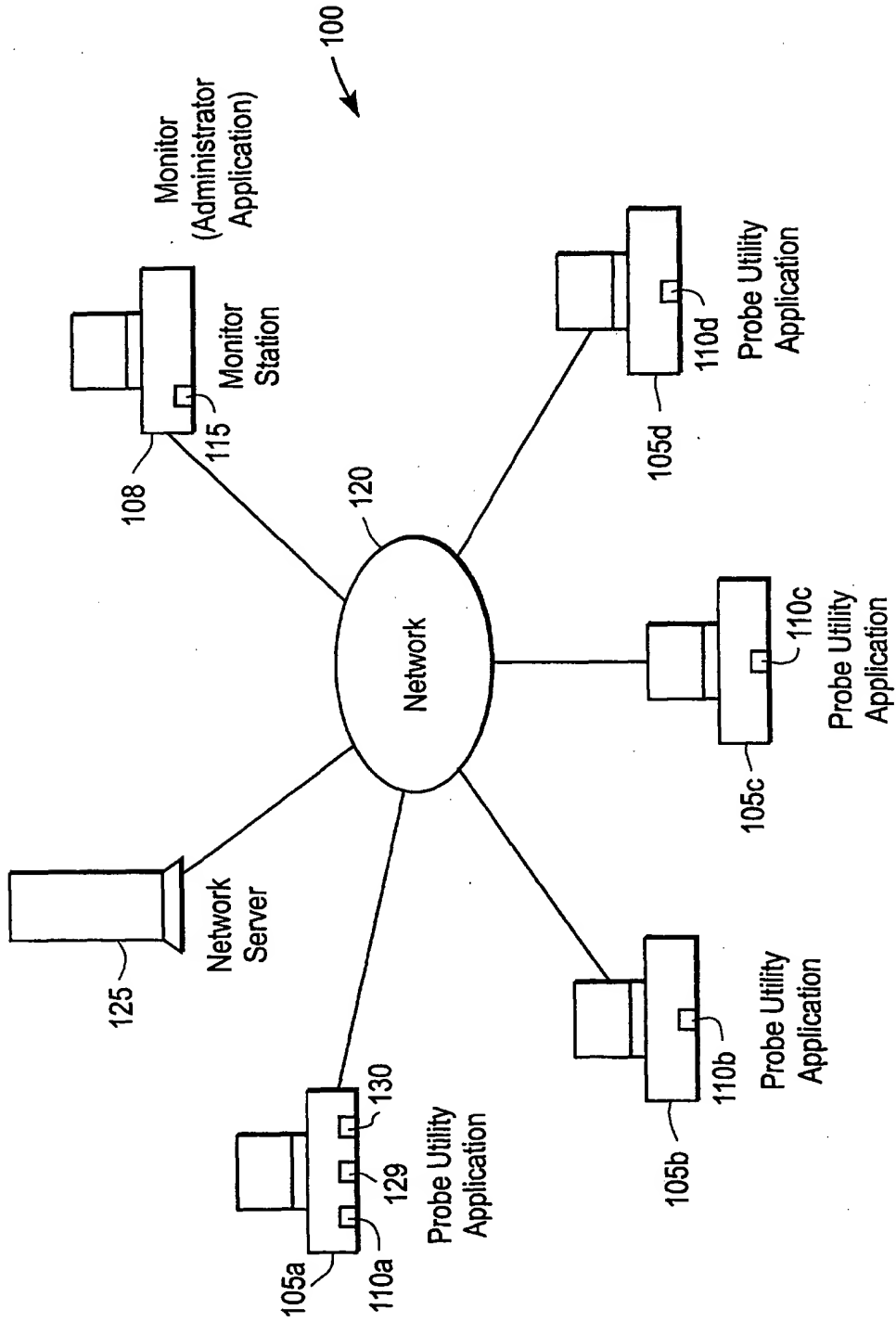


FIG. 1

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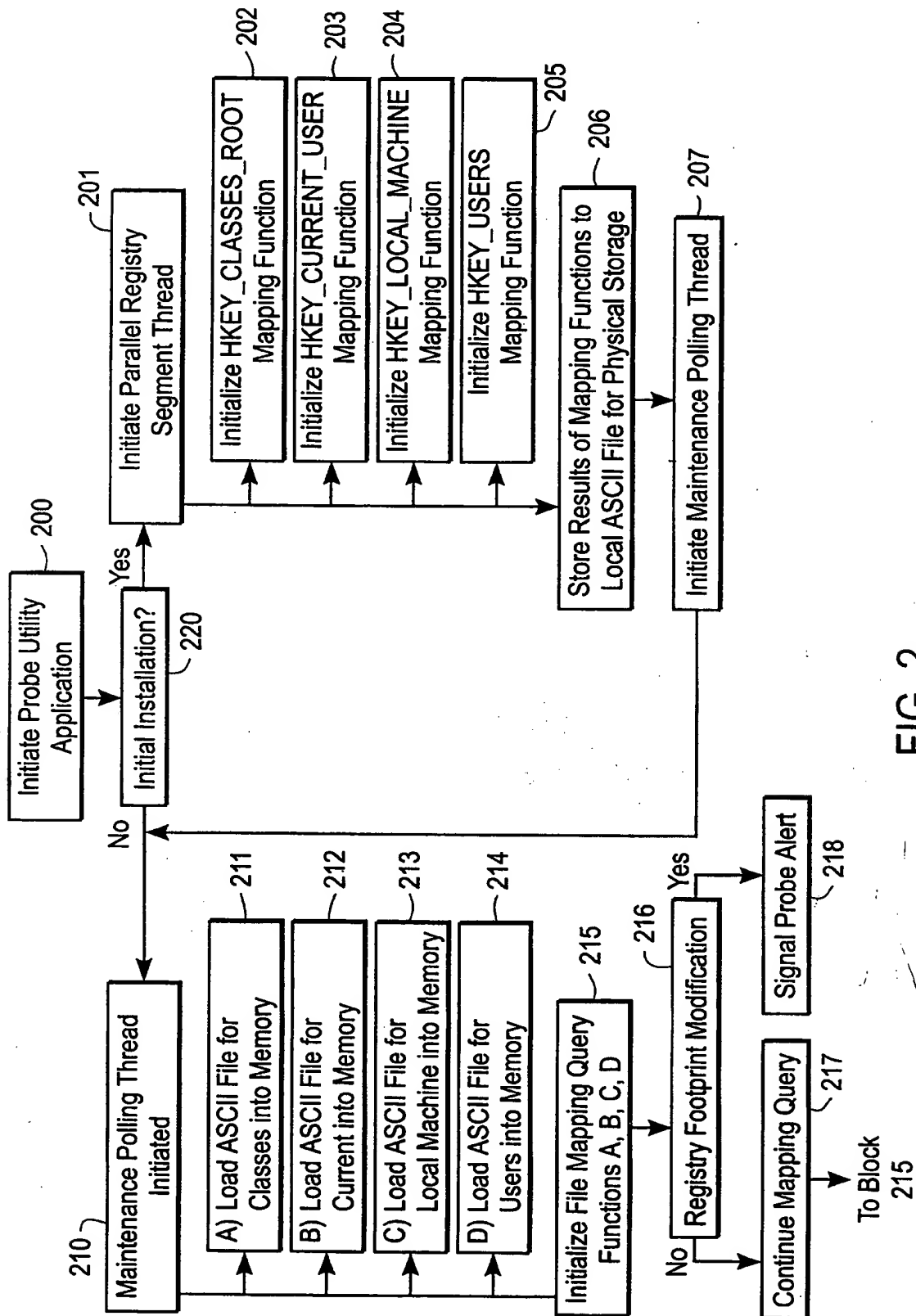


FIG. 2

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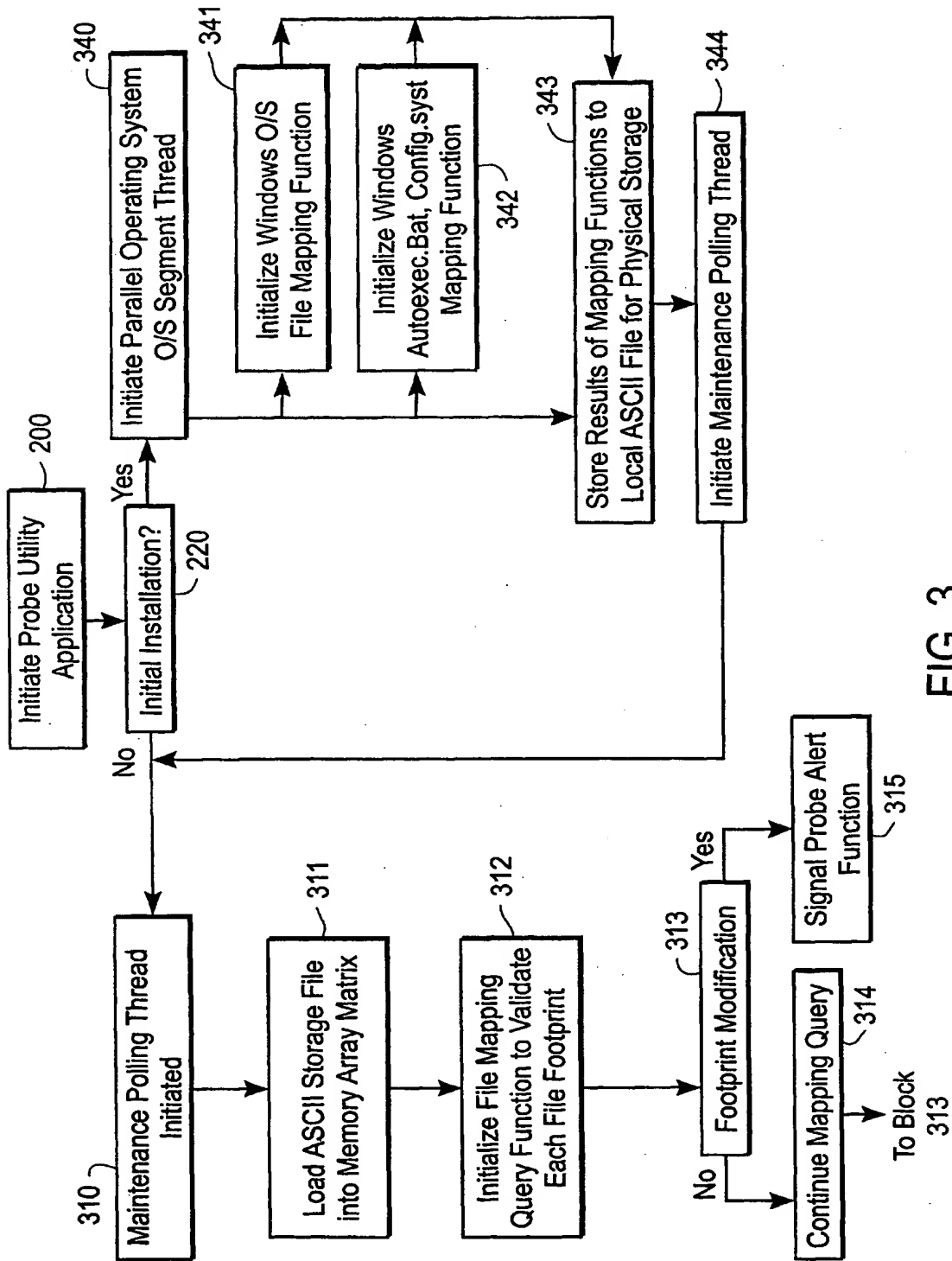
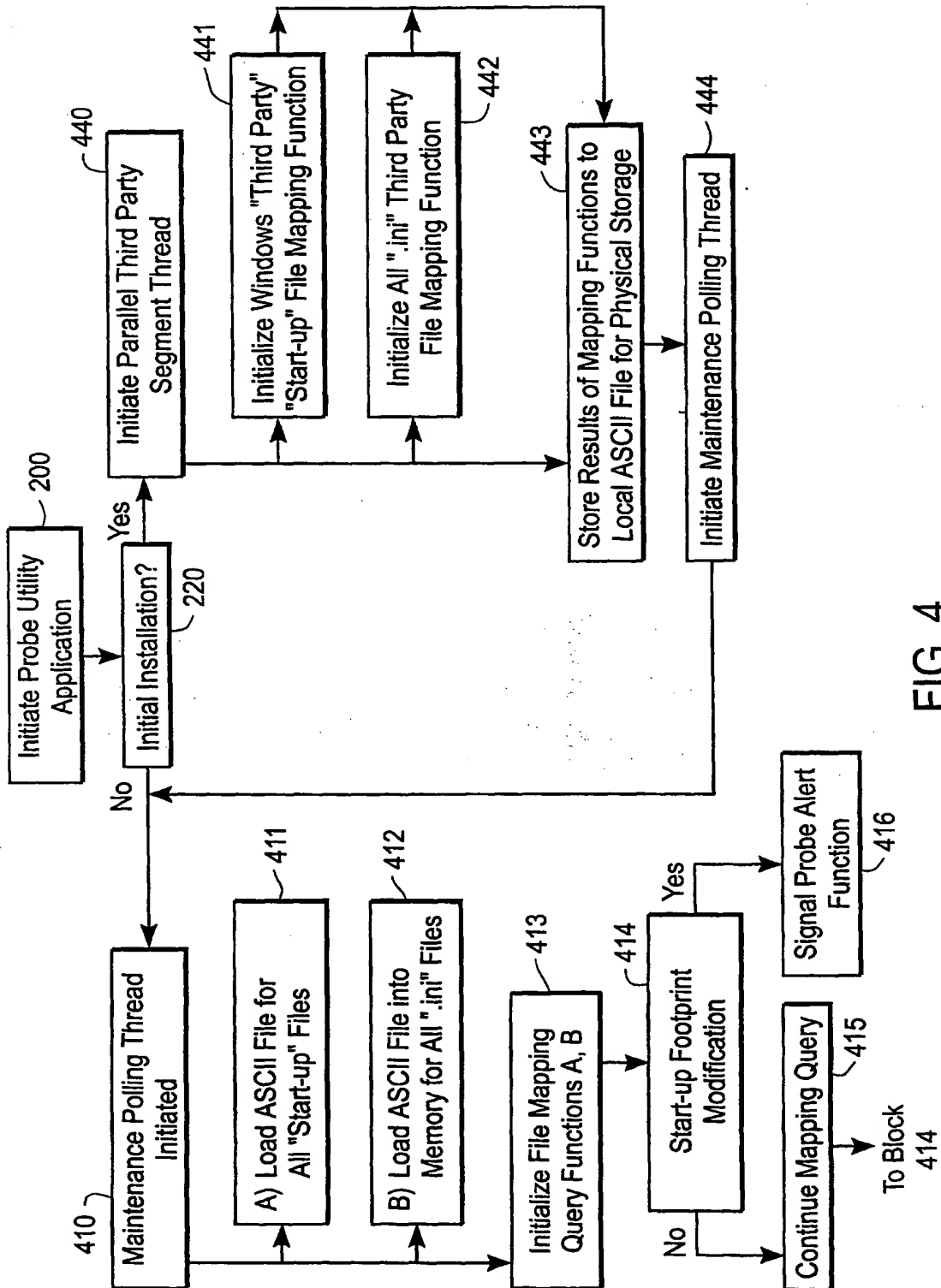


FIG. 3

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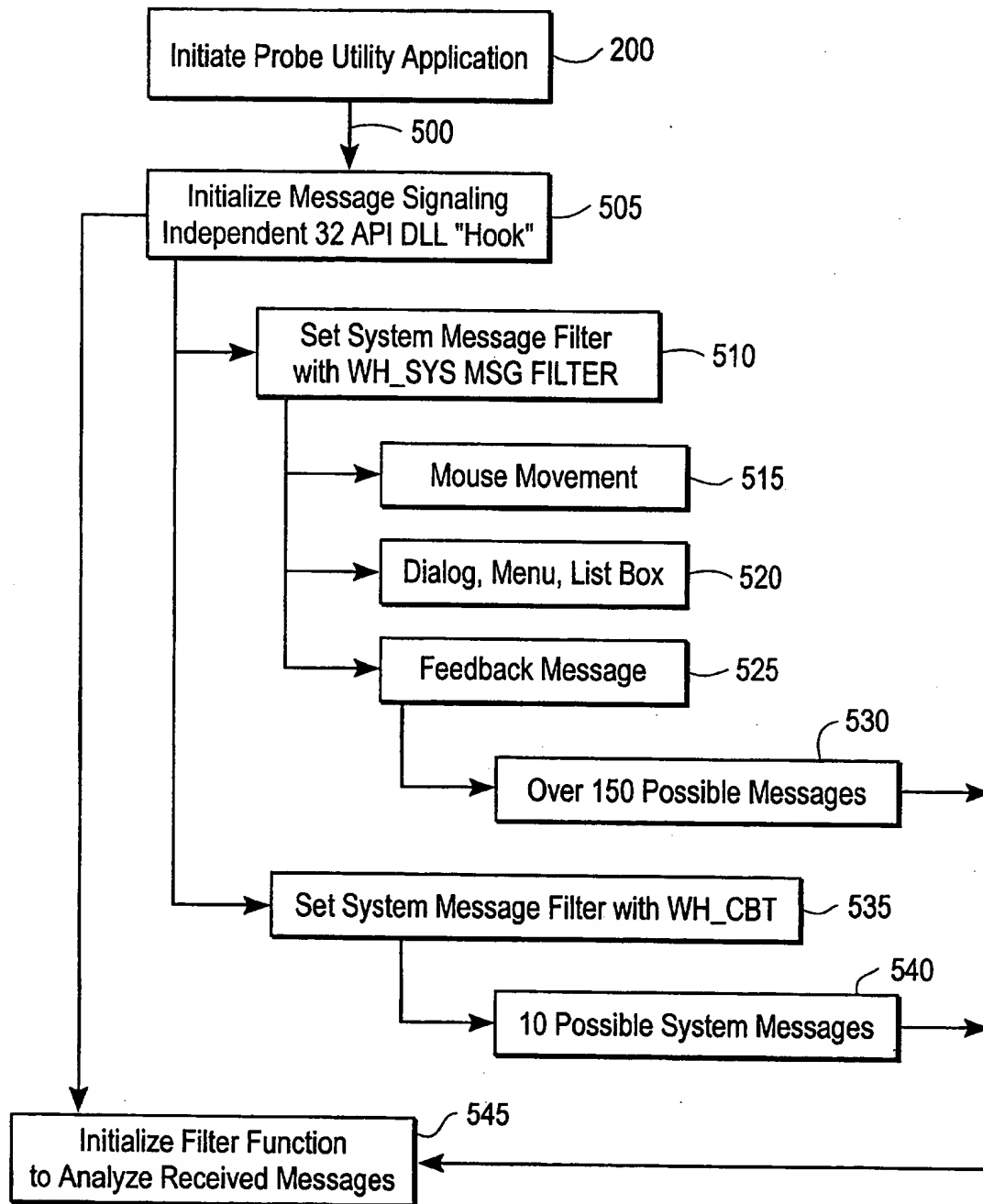


FIG. 5

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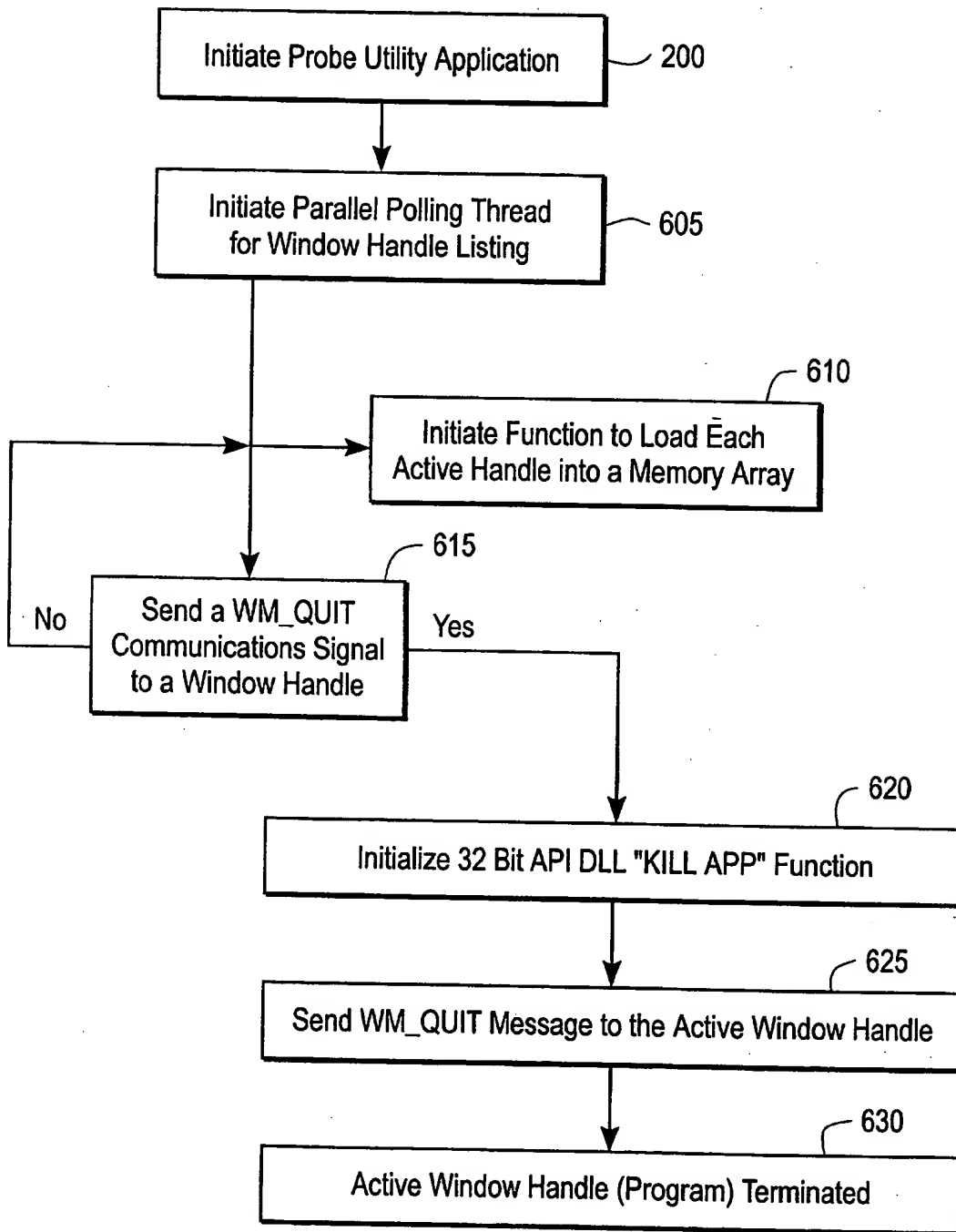


FIG. 6

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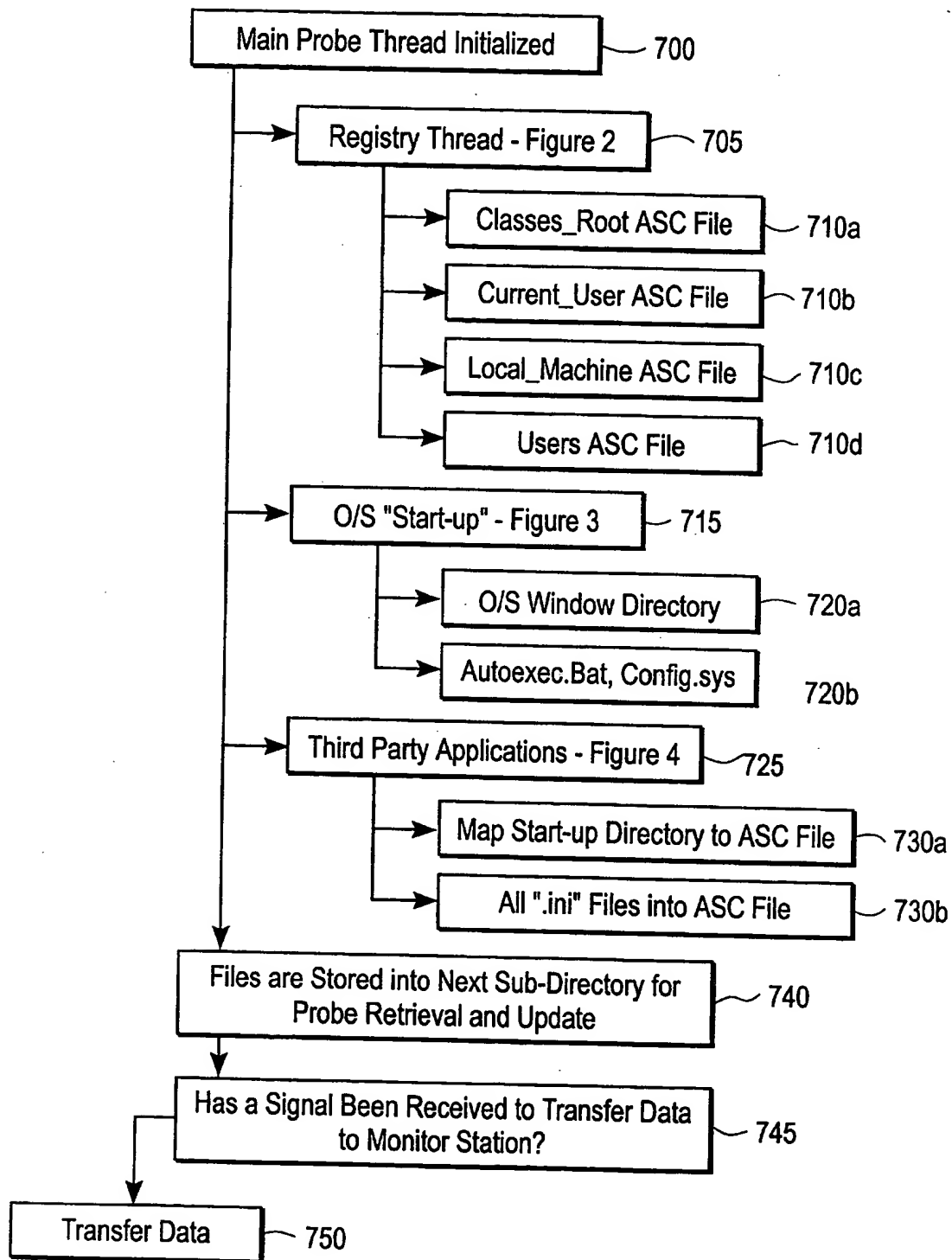


FIG. 7

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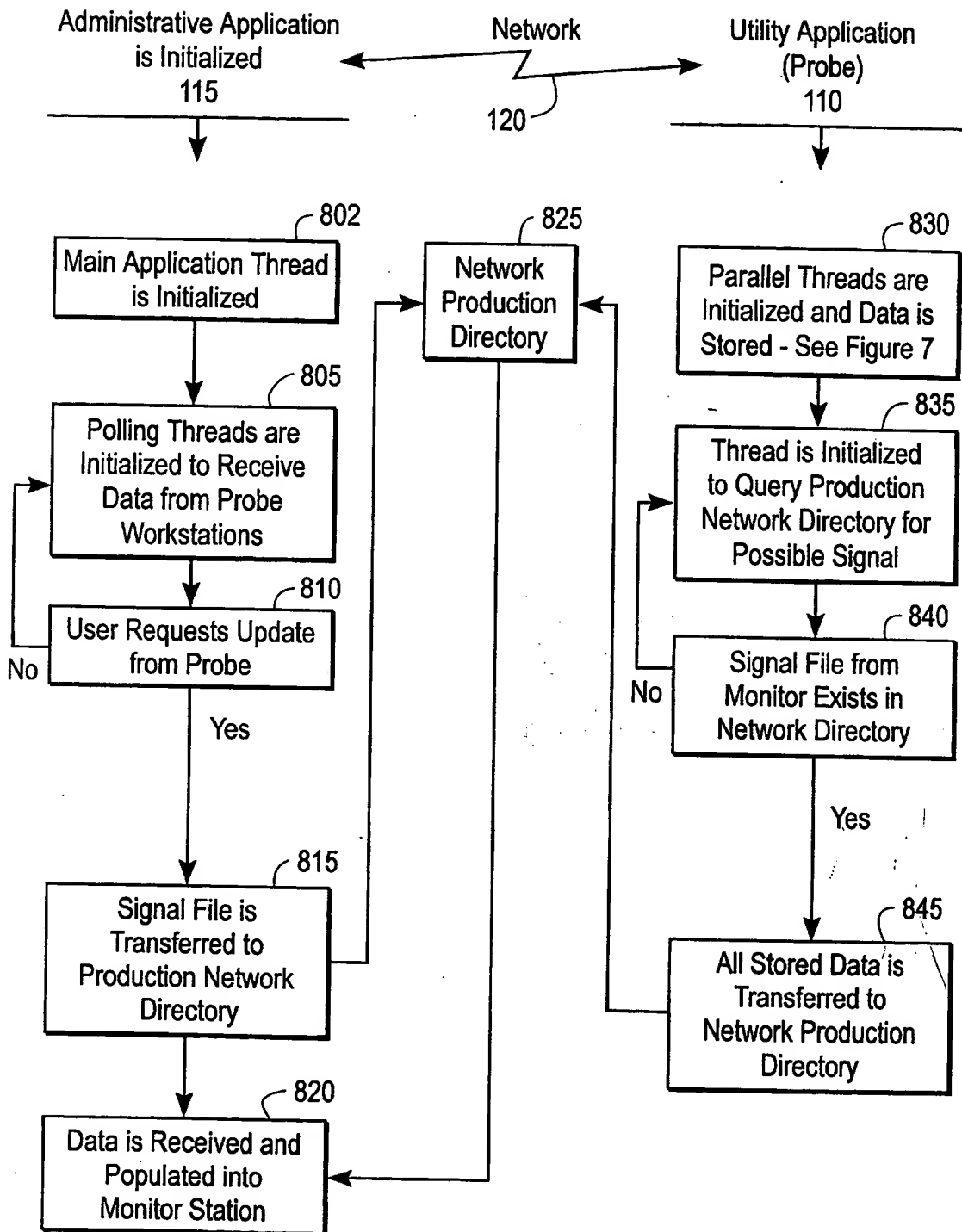


FIG. 8

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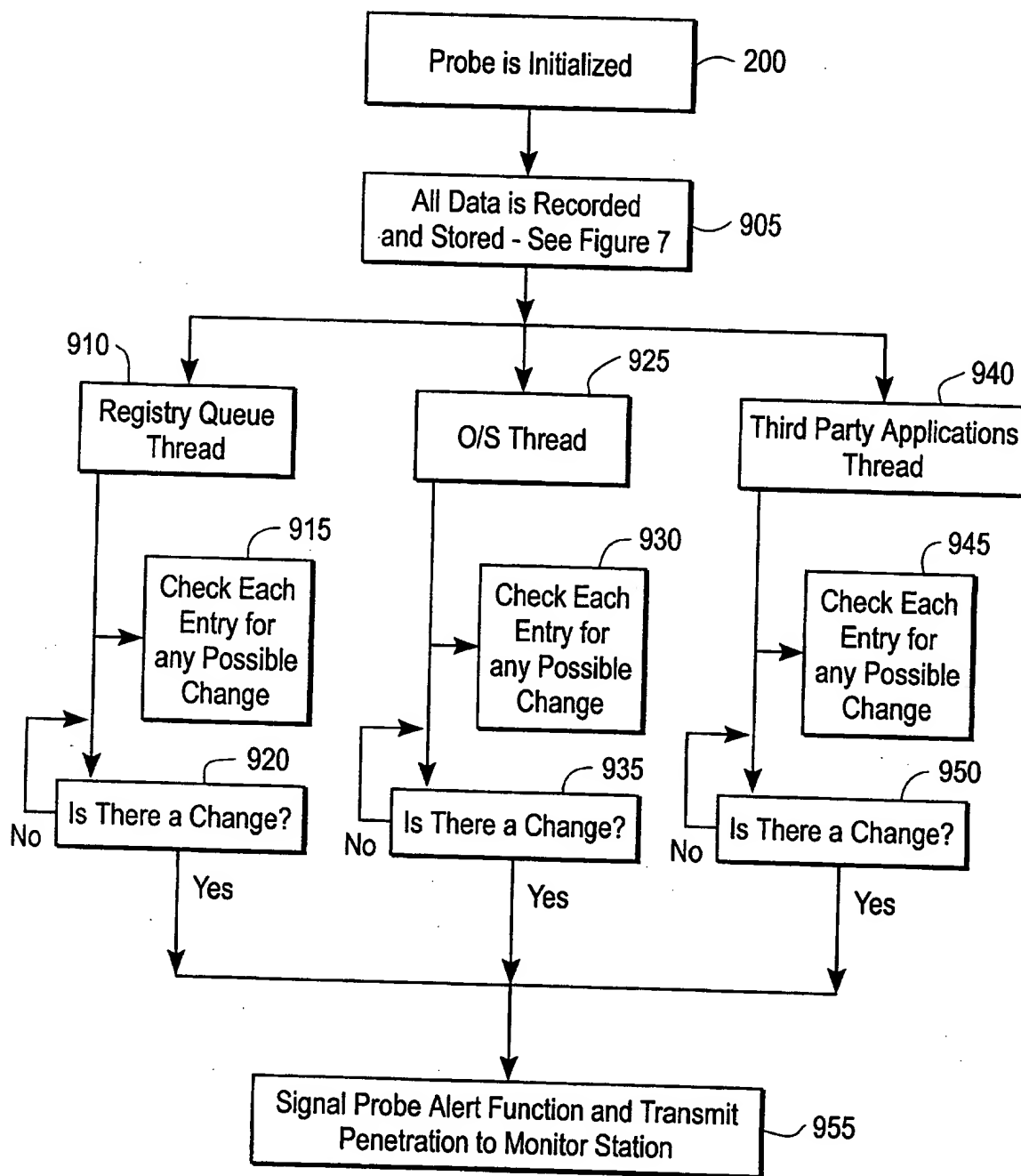


FIG. 9

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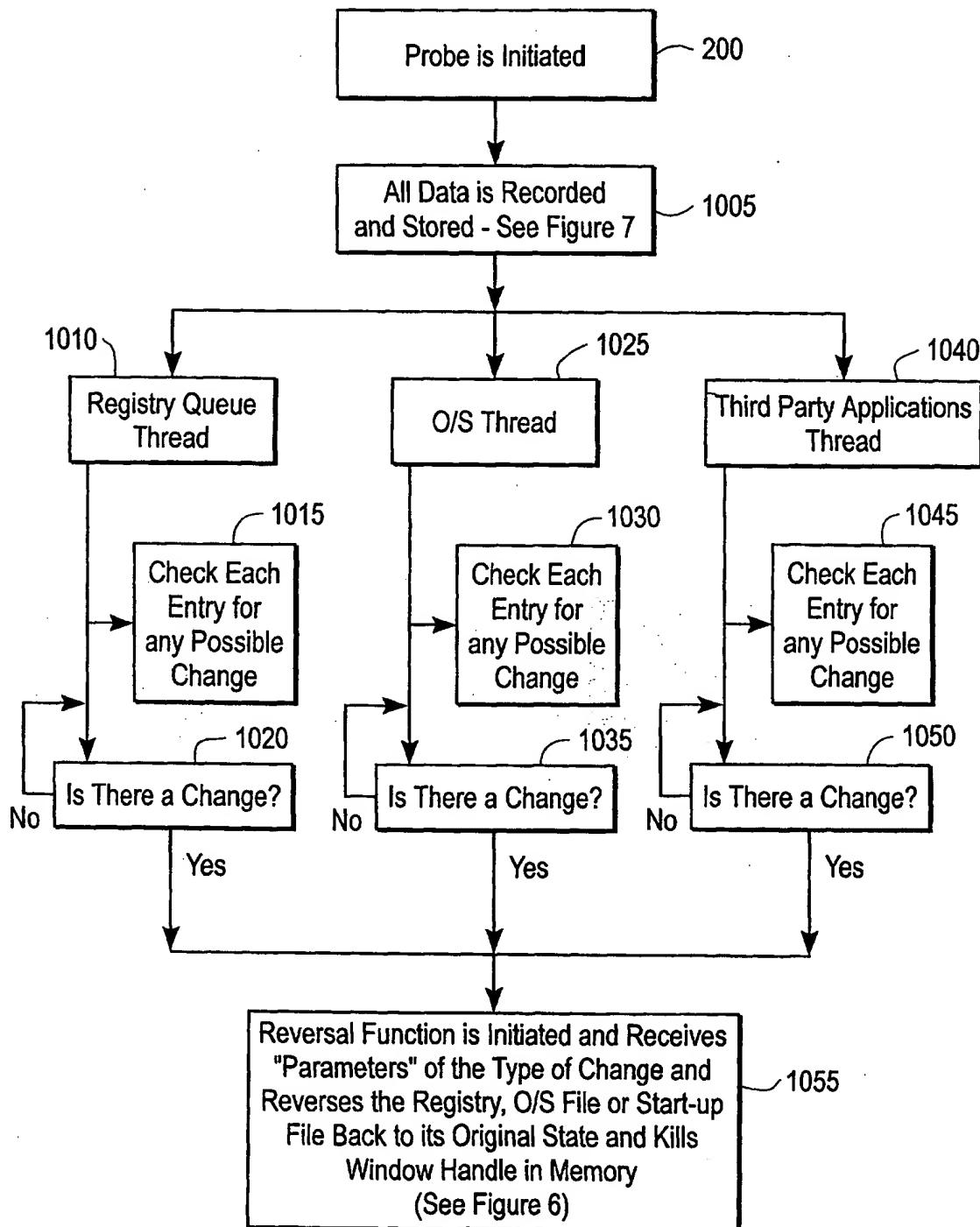


FIG. 10

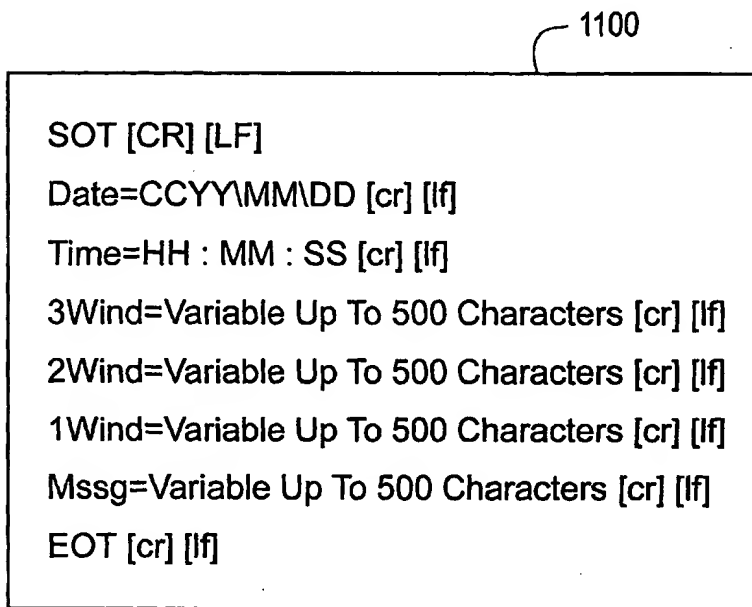
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Structured Signal File Block Diagram

FIG. 11

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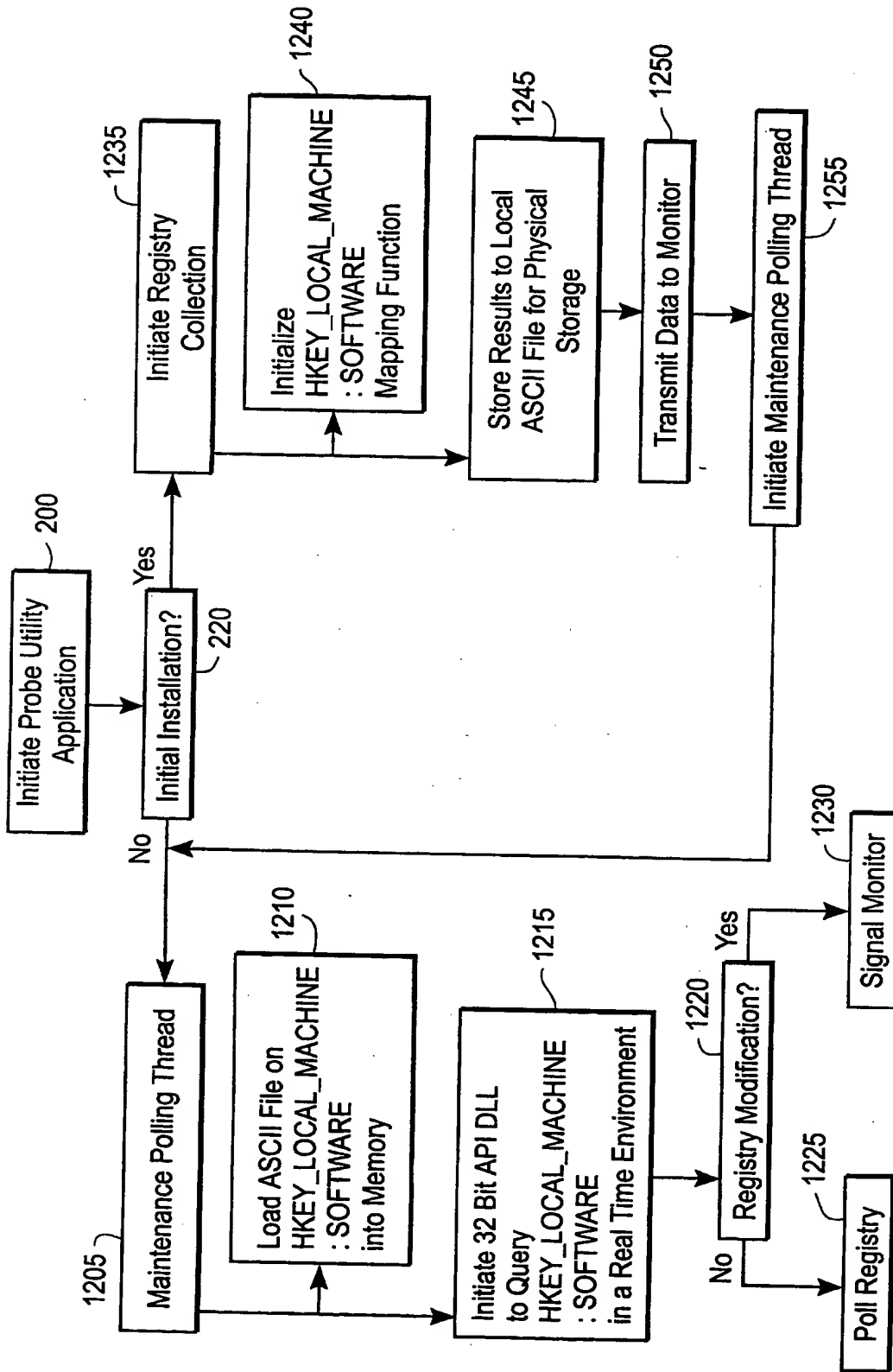


FIG. 12

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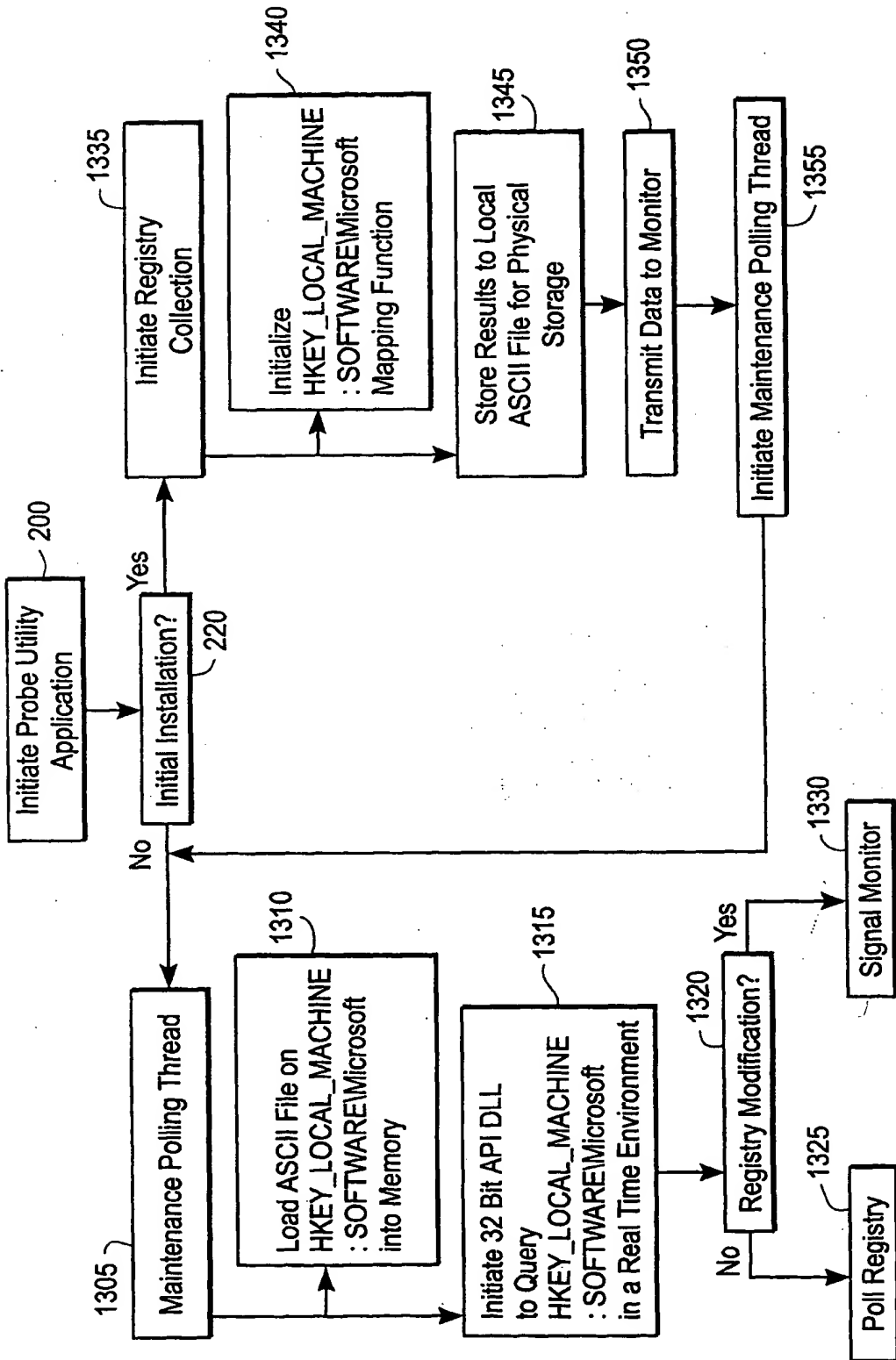


FIG. 13

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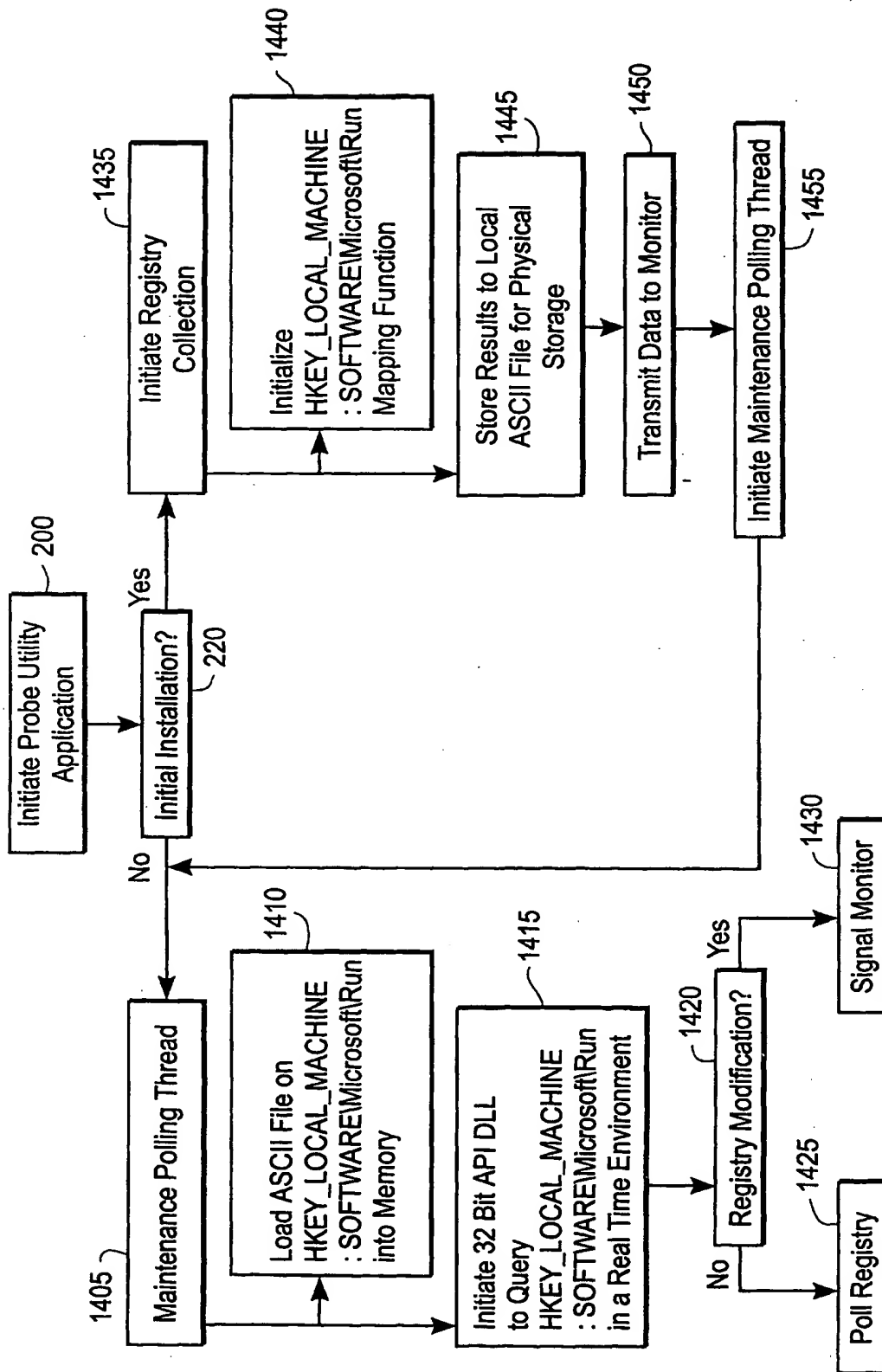


FIG. 14

FIG. 15

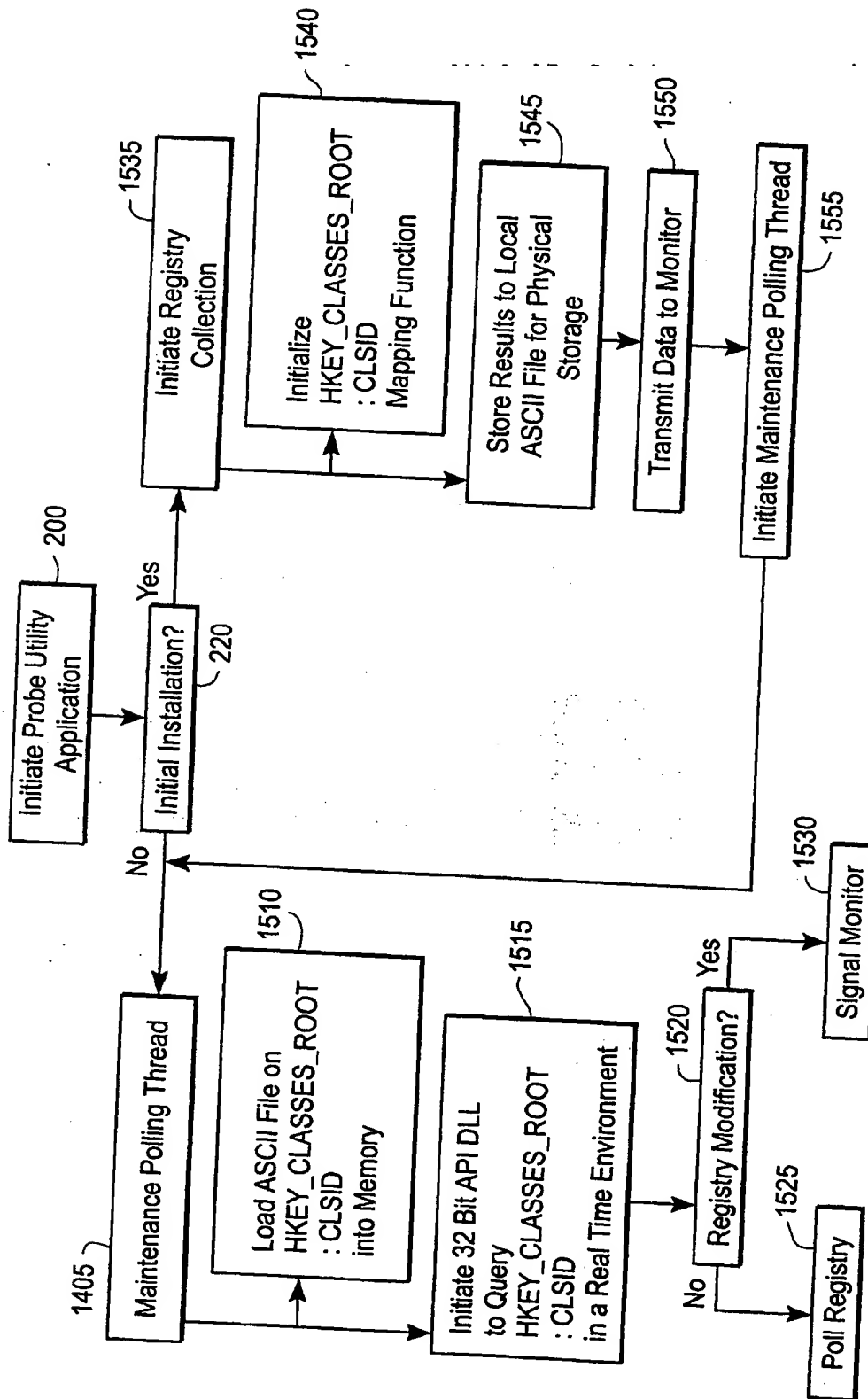


FIG. 15

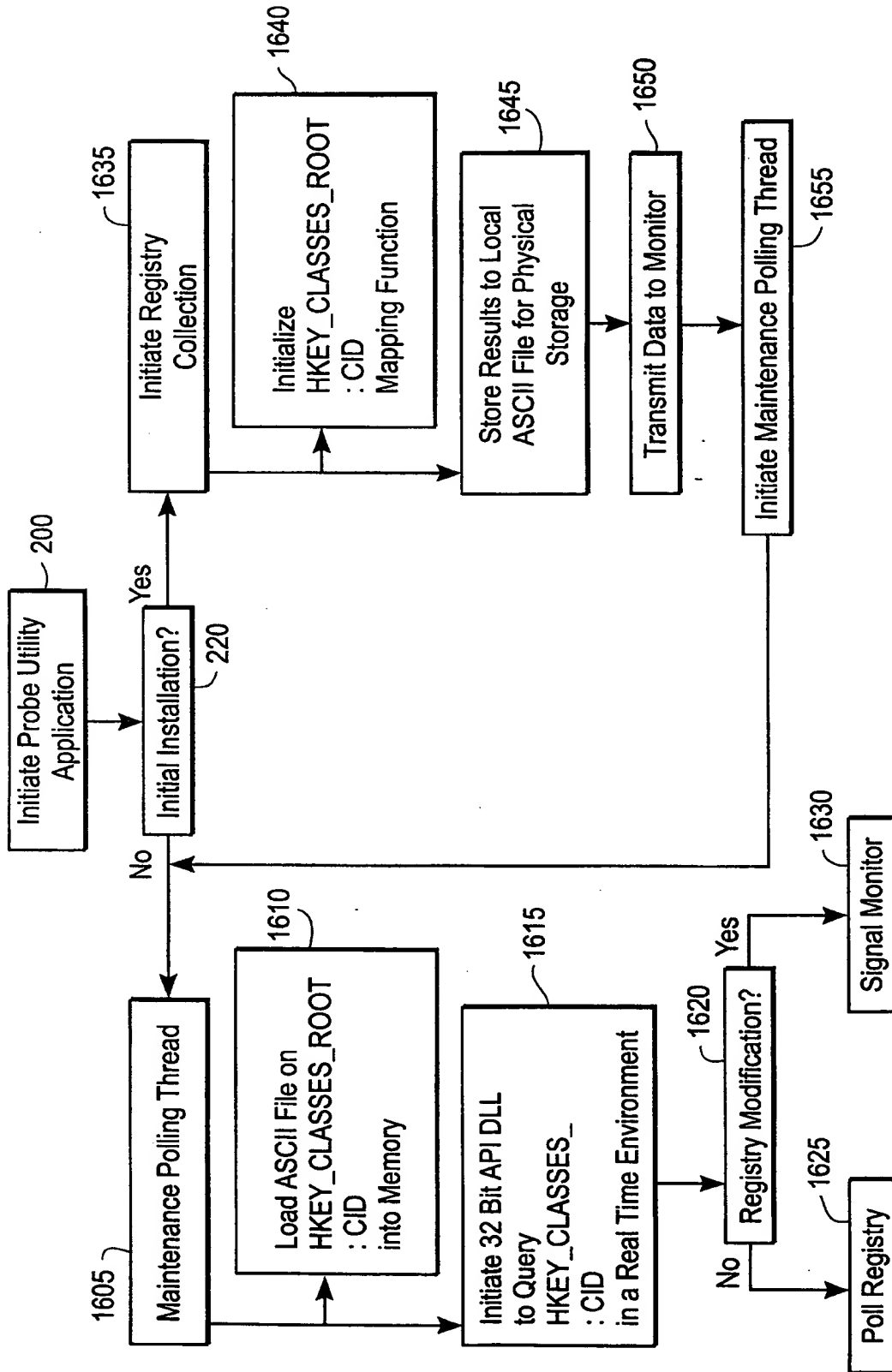


FIG. 16



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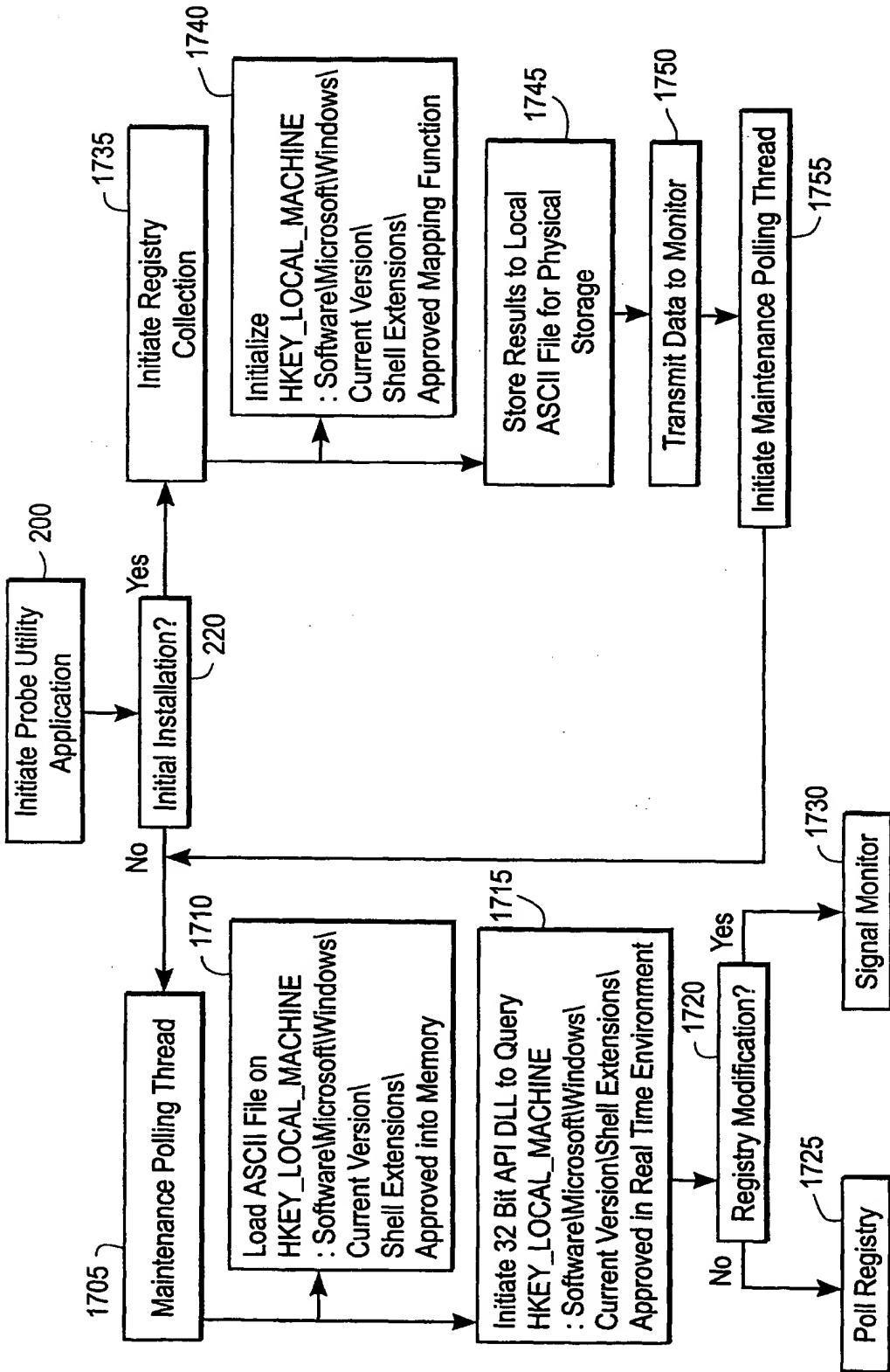


FIG. 17

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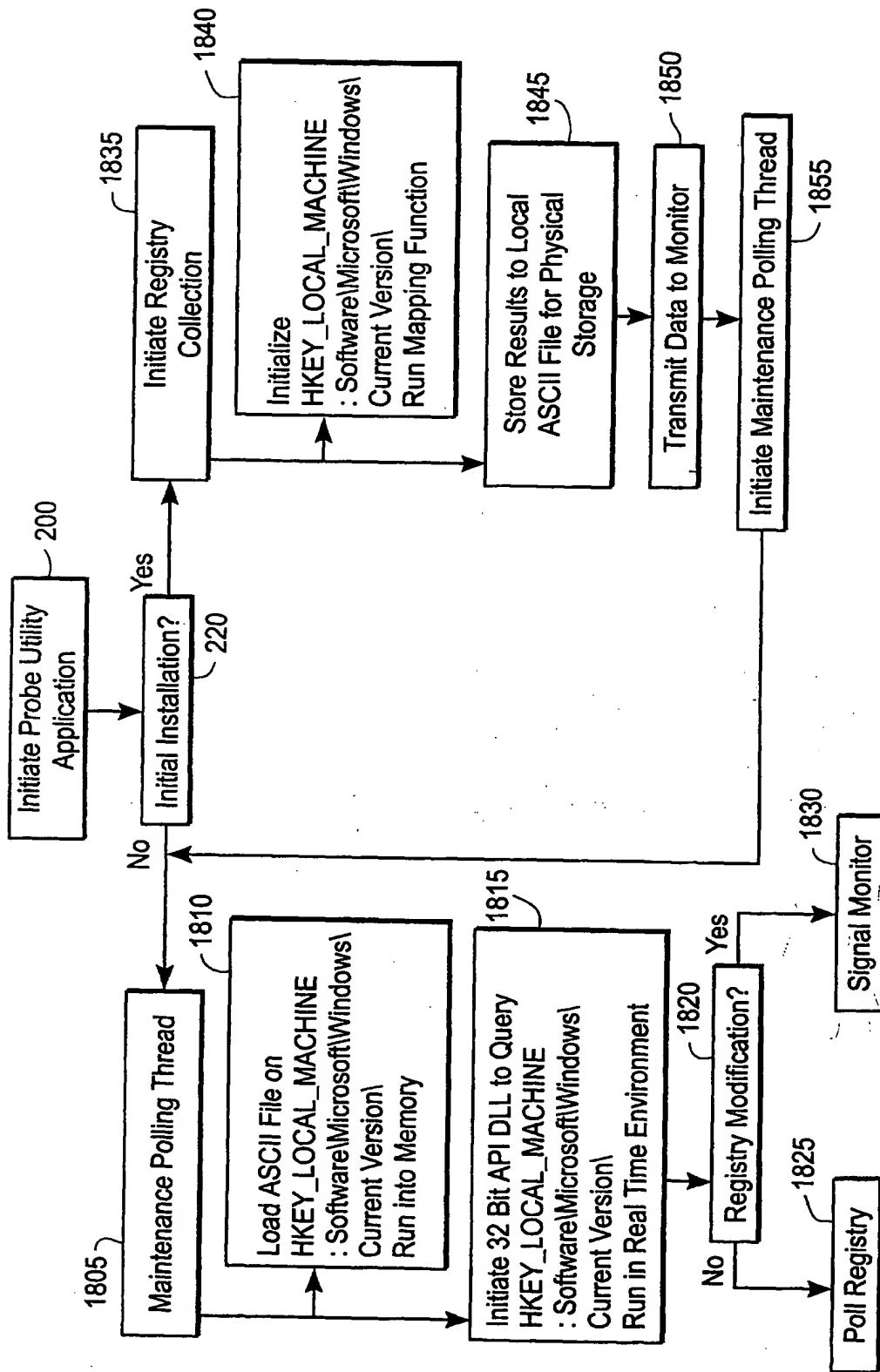


FIG. 18

FO/220" T6B/2860

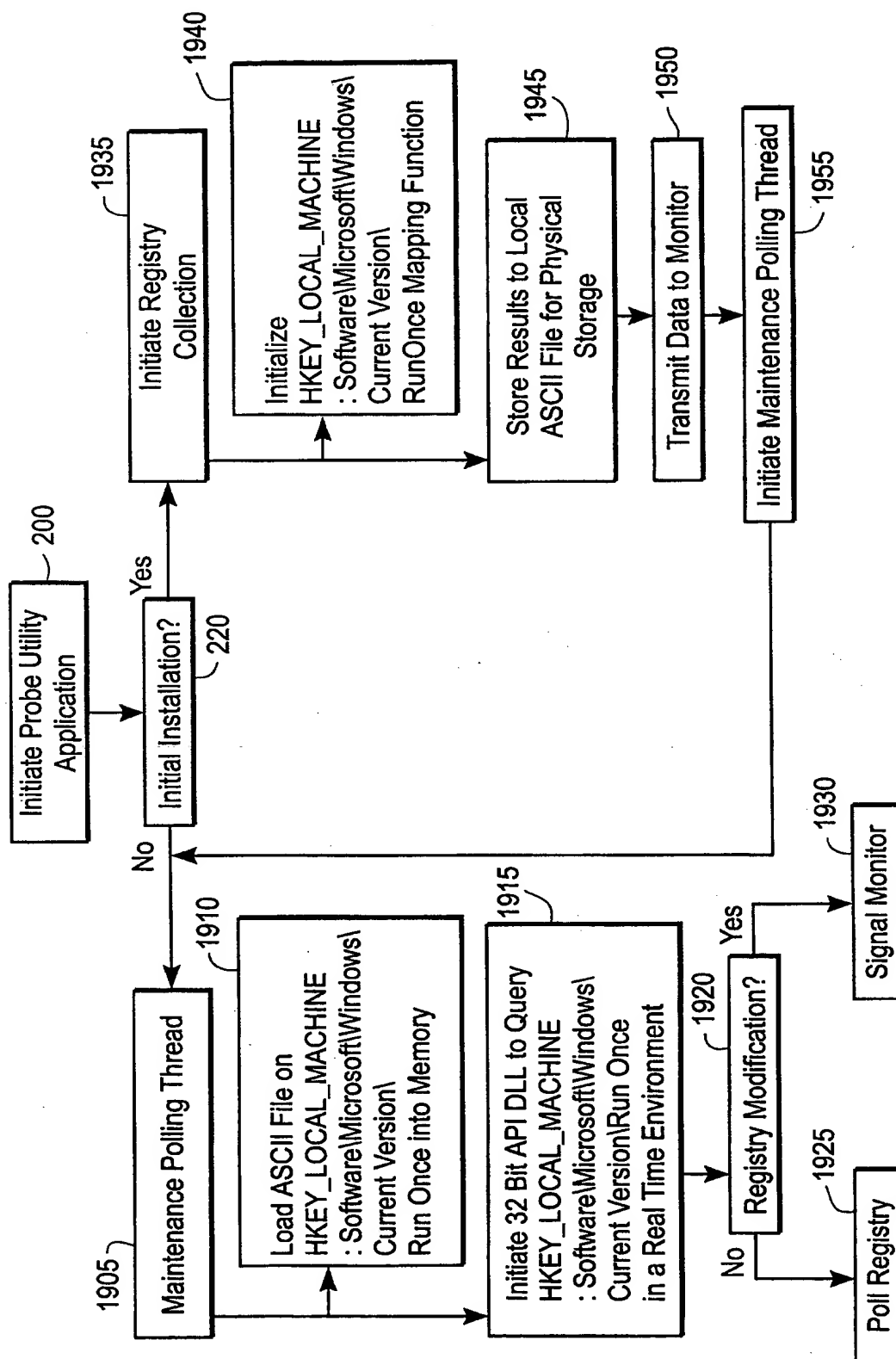


FIG. 19

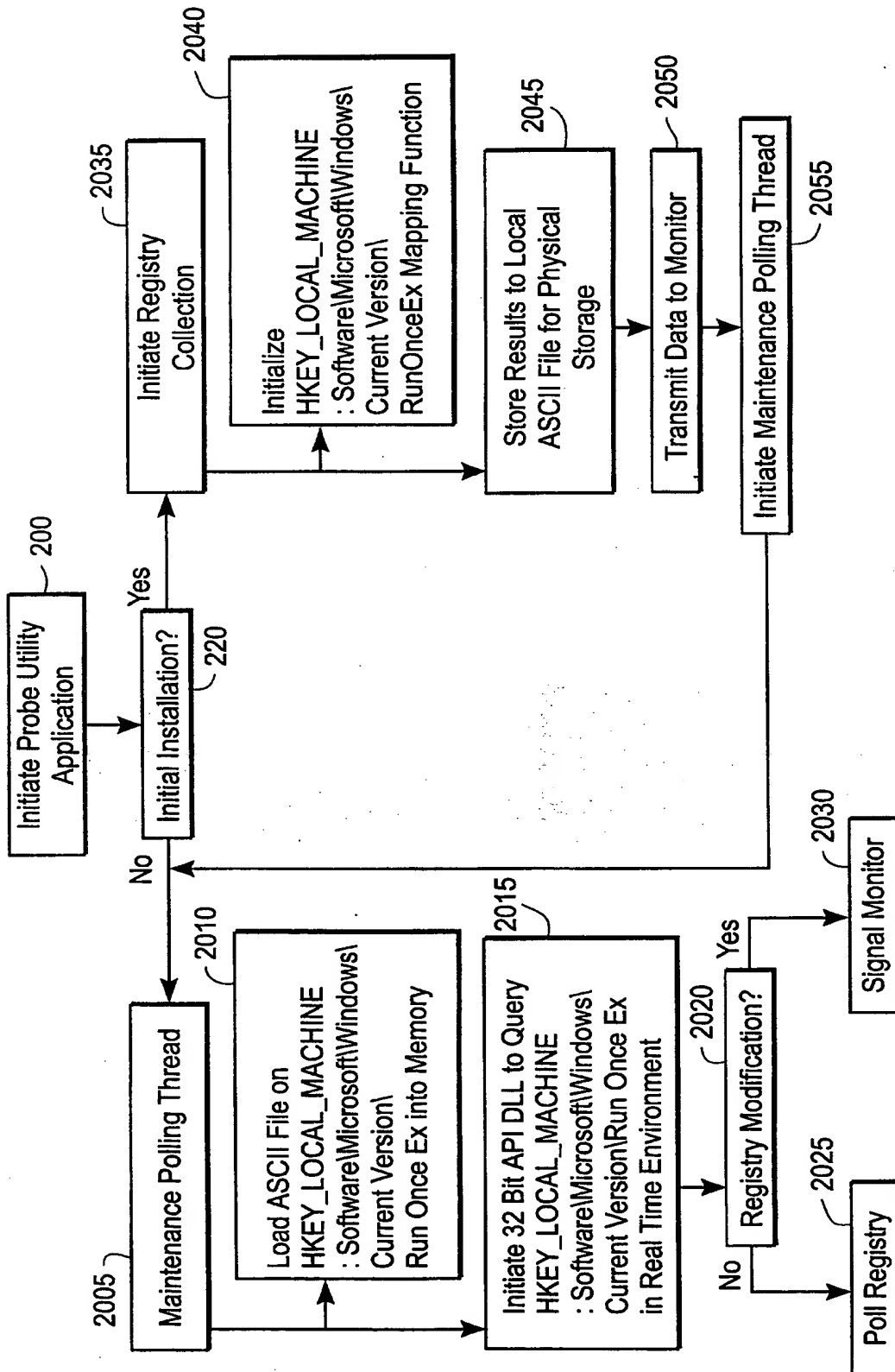


FIG. 20

FOC220" T5B22860

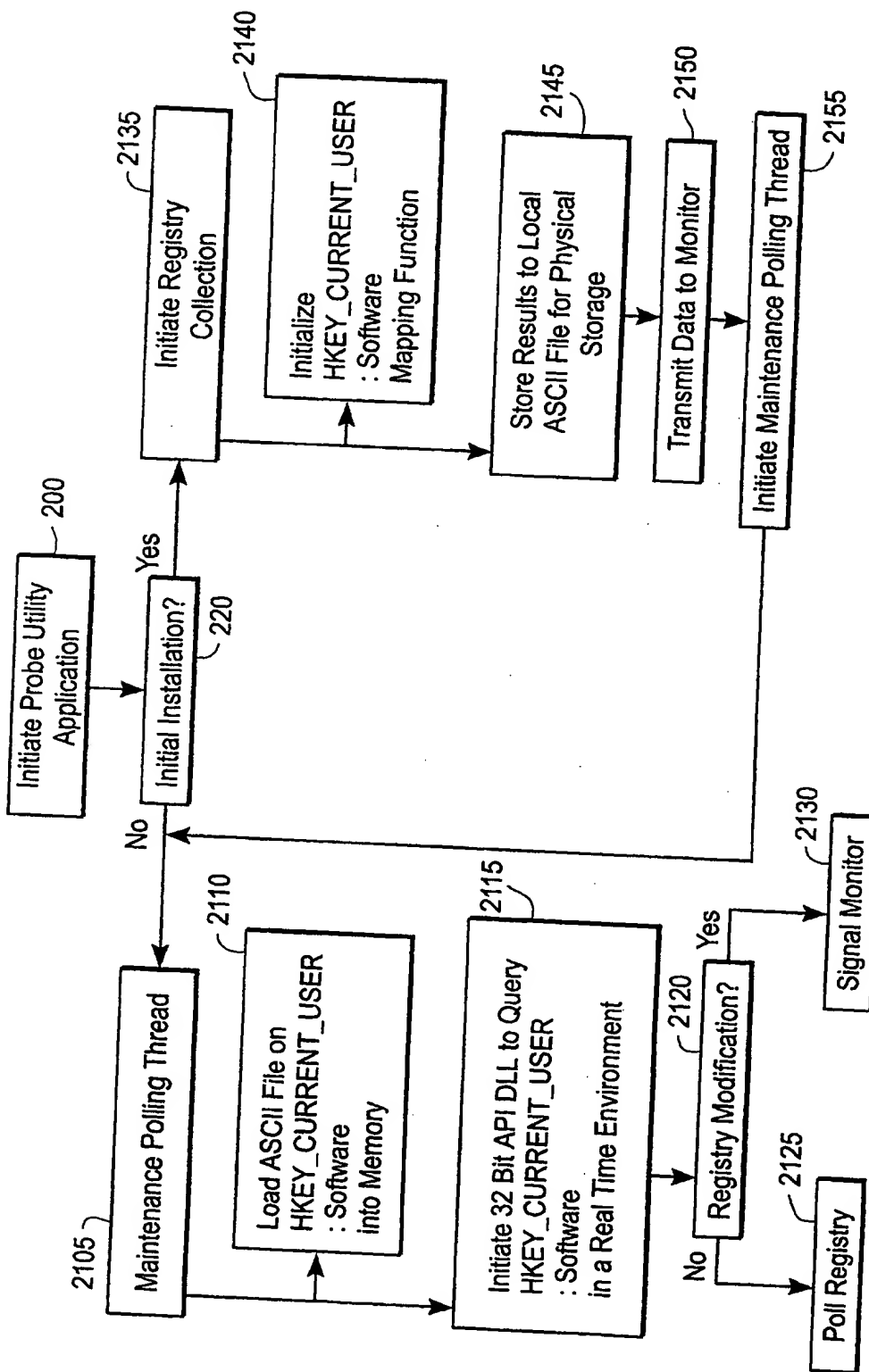
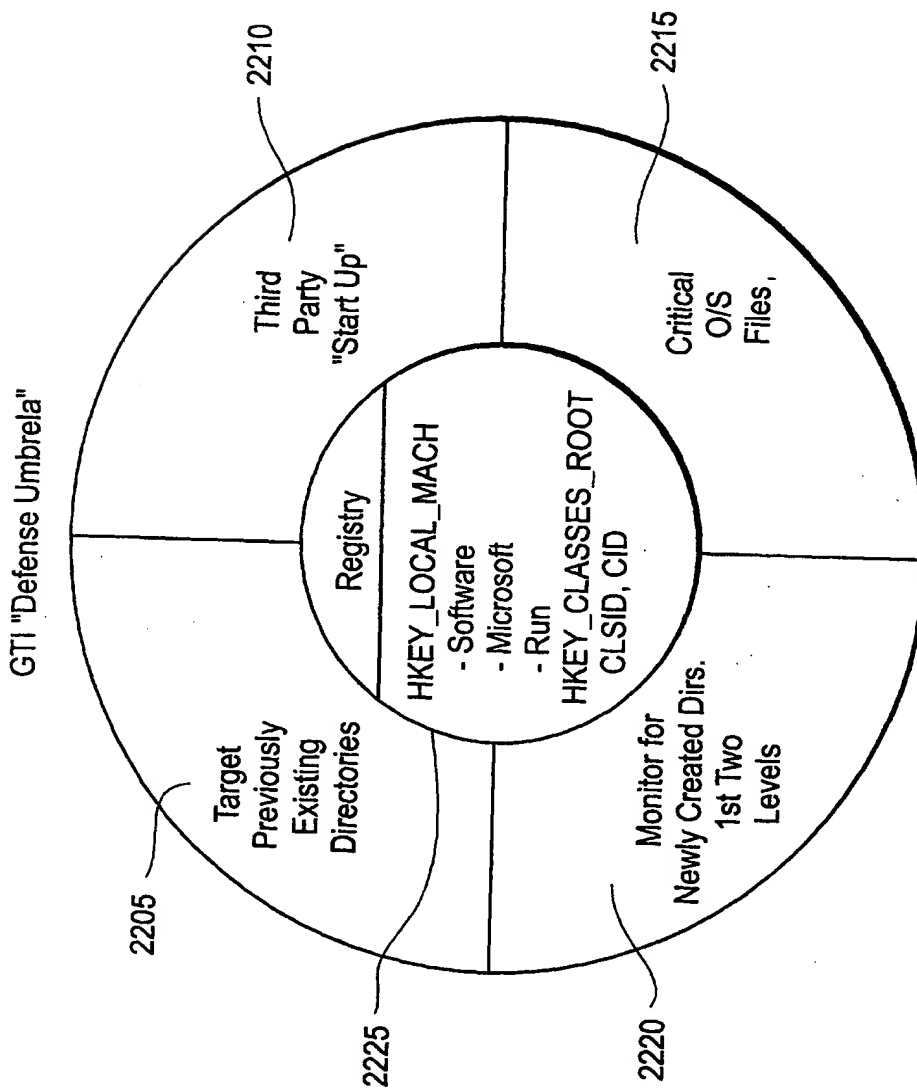


FIG. 21

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"Guarded State" of the Complete PC

FIG. 22

FO/220" T6842860

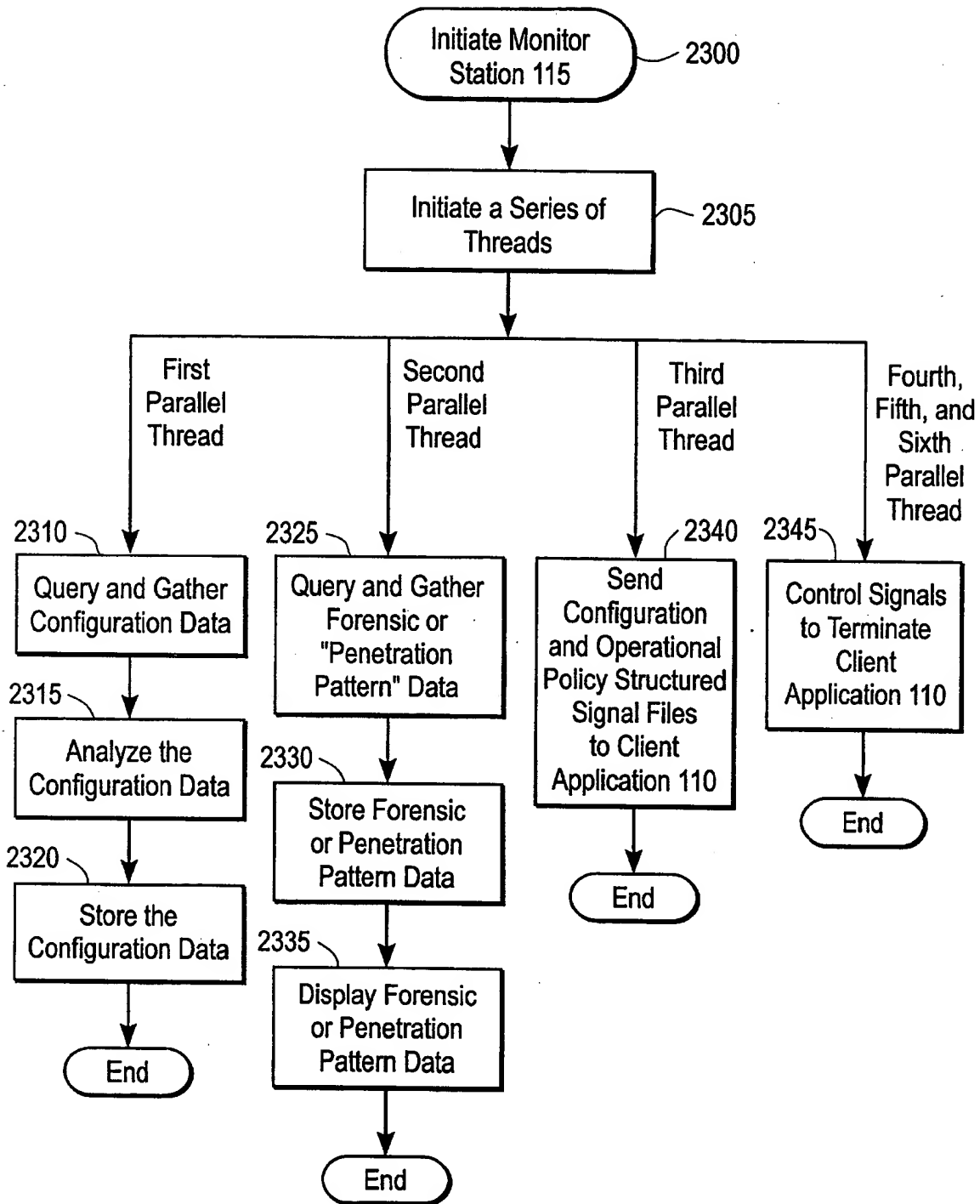


FIG. 23

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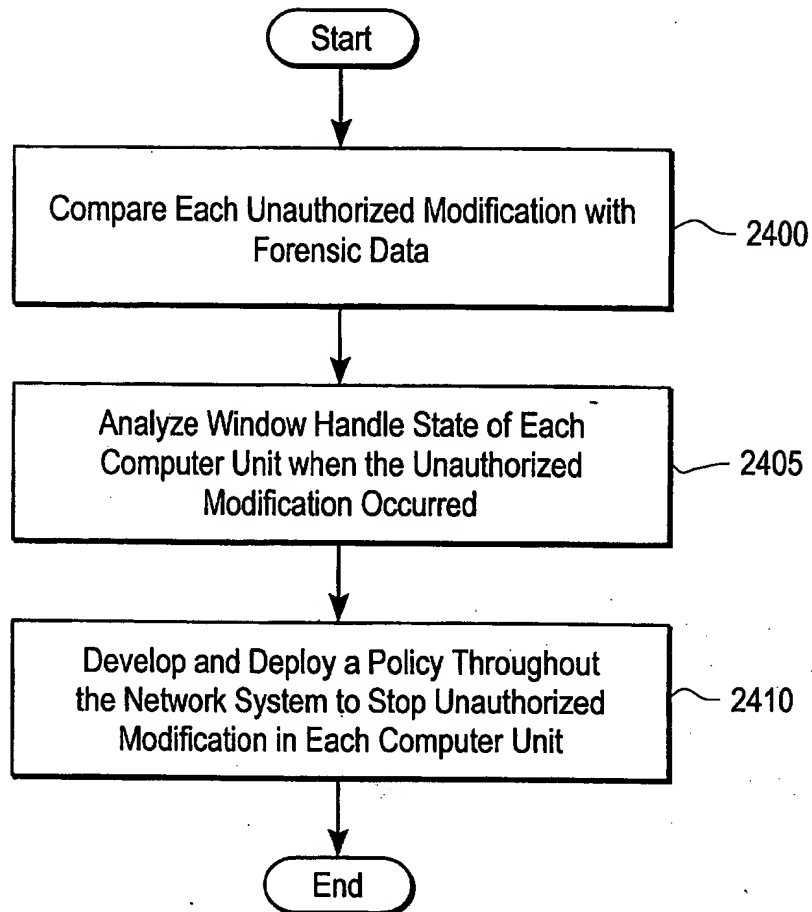


FIG. 24

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